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04/06/2005

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07/05/2005 SMINASS2 00000099 201430 10077258

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Gloria Sikora	(Depositor's name)
<i>Gloria Sikora</i>	(Signature)
June 29, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/077,258	02/14/2002	Brian Sze-Ki Mo	018865-001010US	5261

TITLE OF INVENTION: METHOD OF FORMING A TRENCH TRANSISTOR HAVING A SUPERIOR GATE DIELECTRIC

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/06/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
HUYNH, YENNHU B	2813	438-270000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Fairchild Semiconductor Corporation

South Portland, Maine

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date July 29, 2005

Typed or printed name

Barmak Sani

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